

## Proposed Definitions for BAL-001-AB-0a

### I. Proposed New Definitions

*control performance standard (CPS)* means the reliability standard that sets the limits of a balancing authority's area control error over a specified time period.  
(as defined in the NERC Glossary)

*frequency bias setting* means a value, usually expressed in MW/0.1 Hz, set into a balancing authority ACE algorithm that allows the balancing authority to contribute its frequency response to the Interconnection.  
(as defined in the NERC Glossary)

*frequency deviation* means a change in Interconnection frequency  
(as defined in the NERC Glossary)

*frequency error* means the difference between the actual and scheduled frequency.  
( $F_A - F_S$ )  
(as defined in the NERC Glossary)

*tie line* means a circuit connecting two balancing authority areas.  
(as defined in the NERC Glossary)

### II. Proposed Revisions to Existing Definitions

*interconnection* means the electrical connection of the AIES with any electric system in a jurisdiction bordering Alberta. When capitalized, it means any one of the three major electric system networks in North America: Eastern, Western, and ERCOT.